

NCE85H21TC



NCE N-Channel Enhancement Mode Power MOSFET

Description

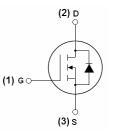
The NCE85H21TC uses advanced trench technology and design to provide excellent $R_{\text{DS(ON)}}$ with low gate charge. It can be used in automotive applications and a wide variety of other applications.

General Features

- V_{DSS} =85V, I_D =210A $R_{DS(ON)}$ <4.9mΩ @ V_{GS} =10V
- Good stability and uniformity with high E_{AS}
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



TO-247 top view

100% UIS TESTED!

100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE85H21TC	NCE85H21TC	TO-247	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	VDSS	85	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current-Continuous	I _D	210	Α	
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	150	Α	
Pulsed Drain Current	I _{DM}	850	Α	
Maximum Power Dissipation	P _D	300	W	
Derating factor		2.0	W/℃	
Single pulse avalanche energy (Note 3)	E _{AS}	1800	mJ	
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	$^{\circ}\!\mathbb{C}$	

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 1)	$R_{ heta JC}$	0.5	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	85	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =85V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±200	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	3.2	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A	-	4.0	4.9	mΩ
Forward Transconductance	9 FS	V _{DS} =10V,I _D =20A	35		-	S
Dynamic Characteristics			•			
Input Capacitance	C _{lss}	\/ -05\/\/ -0\/	-	7600	-	PF
Output Capacitance	C _{oss}	V_{DS} =25V, V_{GS} =0V, F=1.0MHz	-	720	-	PF
Reverse Transfer Capacitance	C _{rss}	r-1.0ivinz	-	346	-	PF
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}		-	23	ı	nS
Turn-on Rise Time	t _r	V_{DD} =40V, I_D =2A, R_L =15 Ω	-	124	ı	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =10V, R_{G} =2.5 Ω	-	84	ı	nS
Turn-Off Fall Time	t _f		-	78	1	nS
Total Gate Charge	Qg		-	140	1	nC
Gate-Source Charge	Q_{gs}	ID=40A,VDD=40V,VGS=10V	-	40	ı	nC
Gate-Drain Charge	Q_{gd}		-	57	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =40A	-	-	1.2	V
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 40A	-	110	ı	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note2)}$	-	300	-	nC

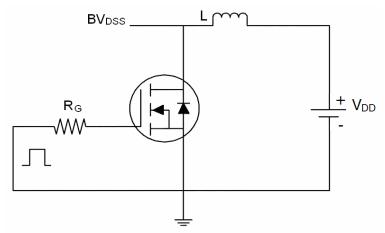
Notes:

- 1. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 2. Pulse Test: Pulse Width \leq 400 μ s, Duty Cycle \leq 2%.
- 3. EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=42.5V,VG=10V,L=2mH,Rg=25 Ω ,IAS=37A
- 4. Isd \leqslant 125A, di/dt \leqslant 260A/ μ s, Vdd \leqslant V(BR)dss, TJ \leqslant 175°C

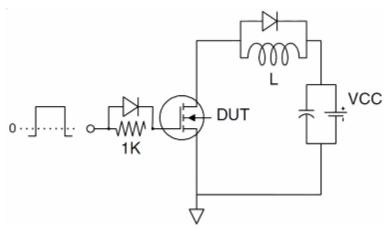
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Test Circuit

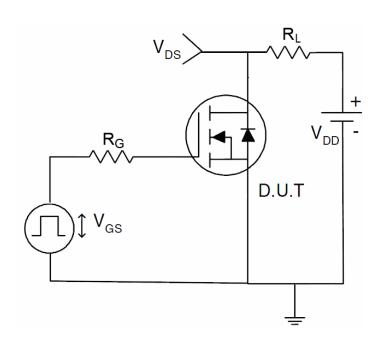
1) E_{AS} test Circuit



2) Gate charge test Circuit

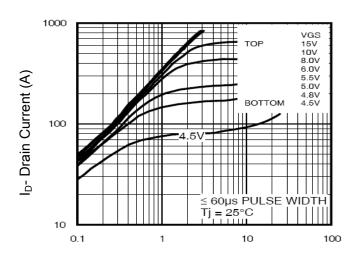


3) Switch Time Test Circuit



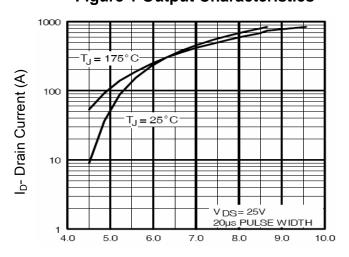


Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

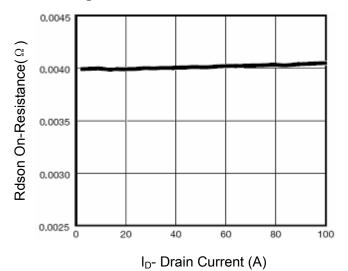


Figure 3 Rdson- Drain Current

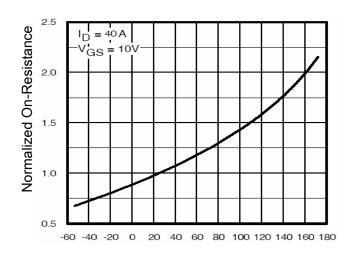
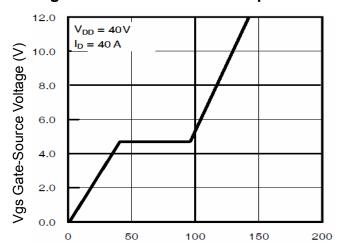
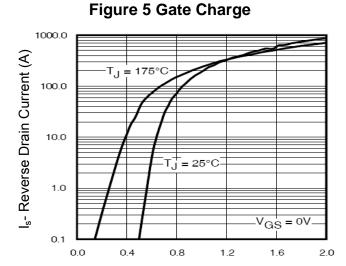


Figure 4 Rdson-JunctionTemperature

 T_J -Junction Temperature($^{\circ}$ C)



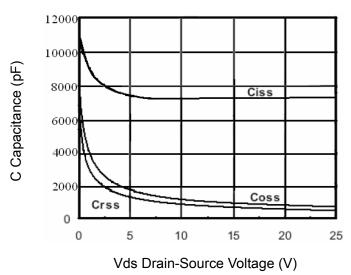
Qg Gate Charge (nC)



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward





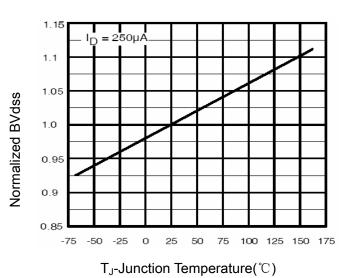


Figure 7 Capacitance vs Vds

Figure 9 BV_{DSS} vs Junction Temperature

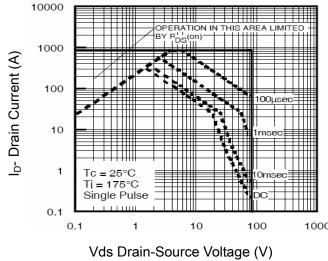


Figure 8 Safe Operation Area

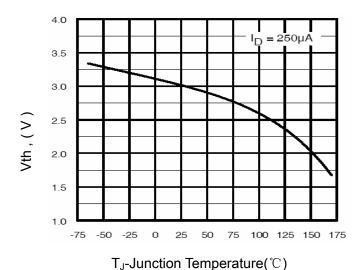
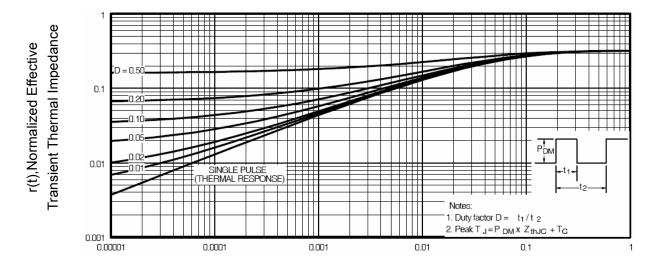


Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec)

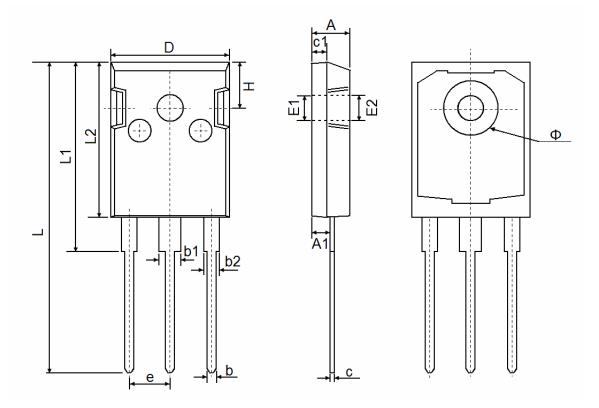
Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



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TO-247 Package Information



Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	4.850	5.150	0.191	0.200	
A1	2.200	2.600	0.087	0.102	
b	1.000	1.400	0.039	0.055	
b1	2.800	3.200	0.110	0.126	
b2	1.800	2.200	0.071	0.087	
С	0.500	0.700	0.020	0.028	
c1	1.900	2.100	0.075	0.083	
D	15.450	15.750	0.608	0.620	
E1	3.50	0 REF	0.138 REF		
E2	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626	
L1	24.800	25.100	0.976	0.988	
L2	20.300	20.600	0.799	0.811	
Ф	7.100	7.300	0.280	0.287	
е	5.45	0 TYP	0.215 TYP		
Н	5.98	0 REF	0.235 REF		



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