



**Advanced Power  
Electronics Corp.**

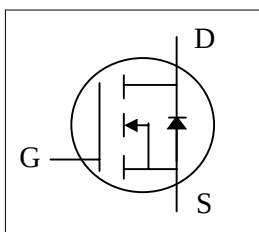
**AP70L02GH/J**

**Pb Free Plating Product**

*N-CHANNEL ENHANCEMENT MODE*

*POWER MOSFET*

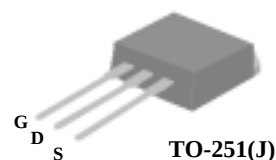
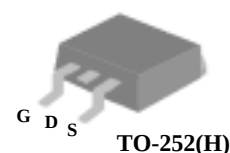
- ▼ Low Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching



|              |             |
|--------------|-------------|
| $BV_{DSS}$   | 25V         |
| $R_{DS(ON)}$ | 9m $\Omega$ |
| $I_D$        | 66A         |

## Description

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP70L02GJ) is available for low-profile applications.



## Absolute Maximum Ratings

| Symbol                 | Parameter                                | Rating     | Units          |
|------------------------|------------------------------------------|------------|----------------|
| $V_{DS}$               | Drain-Source Voltage                     | 25         | V              |
| $V_{GS}$               | Gate-Source Voltage                      | $\pm 20$   | V              |
| $I_D@T_C=25^{\circ}C$  | Continuous Drain Current, $V_{GS}$ @ 10V | 66         | A              |
| $I_D@T_C=100^{\circ}C$ | Continuous Drain Current, $V_{GS}$ @ 10V | 42         | A              |
| $I_{DM}$               | Pulsed Drain Current <sup>1</sup>        | 210        | A              |
| $P_D@T_C=25^{\circ}C$  | Total Power Dissipation                  | 66         | W              |
|                        | Linear Derating Factor                   | 0.53       | W/ $^{\circ}C$ |
| $T_{STG}$              | Storage Temperature Range                | -55 to 150 | $^{\circ}C$    |
| $T_J$                  | Operating Junction Temperature Range     | -55 to 150 | $^{\circ}C$    |

## Thermal Data

| Symbol    | Parameter                                | Value | Unit          |
|-----------|------------------------------------------|-------|---------------|
| Rthj-case | Thermal Resistance Junction-case Max.    | 1.9   | $^{\circ}C/W$ |
| Rthj-amb  | Thermal Resistance Junction-ambient Max. | 110   | $^{\circ}C/W$ |



## AP70L02GH/J

### Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)

| Symbol                              | Parameter                                            | Test Conditions                                          | Min. | Typ.  | Max. | Units |
|-------------------------------------|------------------------------------------------------|----------------------------------------------------------|------|-------|------|-------|
| BV <sub>DSS</sub>                   | Drain-Source Breakdown Voltage                       | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA               | 25   | -     | -    | V     |
| ΔBV <sub>DSS</sub> /ΔT <sub>j</sub> | Breakdown Voltage Temperature Coefficient            | Reference to 25°C, I <sub>D</sub> =1mA                   | -    | 0.037 | -    | V/°C  |
| R <sub>DS(ON)</sub>                 | Static Drain-Source On-Resistance                    | V <sub>GS</sub> =10V, I <sub>D</sub> =33A                | -    | -     | 9    | mΩ    |
|                                     |                                                      | V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A               | -    | -     | 18   | mΩ    |
| V <sub>GS(th)</sub>                 | Gate Threshold Voltage                               | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA | 1    | -     | 3    | V     |
| g <sub>fs</sub>                     | Forward Transconductance                             | V <sub>DS</sub> =10V, I <sub>D</sub> =33A                | -    | 28    | -    | S     |
| I <sub>DSS</sub>                    | Drain-Source Leakage Current (T <sub>j</sub> =25°C)  | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V                | -    | -     | 1    | uA    |
|                                     | Drain-Source Leakage Current (T <sub>j</sub> =150°C) | V <sub>DS</sub> =20V, V <sub>GS</sub> =0V                | -    | -     | 25   | uA    |
| I <sub>GSS</sub>                    | Gate-Source Leakage                                  | V <sub>GS</sub> = ± 20V                                  | -    | -     | ±100 | nA    |
| Q <sub>g</sub>                      | Total Gate Charge <sup>2</sup>                       | I <sub>D</sub> =33A                                      | -    | 23    | -    | nC    |
| Q <sub>gs</sub>                     | Gate-Source Charge                                   | V <sub>DS</sub> =20V                                     | -    | 3     | -    | nC    |
| Q <sub>gd</sub>                     | Gate-Drain ("Miller") Charge                         | V <sub>GS</sub> =5V                                      | -    | 17    | -    | nC    |
| t <sub>d(on)</sub>                  | Turn-on Delay Time <sup>2</sup>                      | V <sub>DS</sub> =15V                                     | -    | 8.8   | -    | ns    |
| t <sub>r</sub>                      | Rise Time                                            | I <sub>D</sub> =33A                                      | -    | 95    | -    | ns    |
| t <sub>d(off)</sub>                 | Turn-off Delay Time                                  | R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =10V               | -    | 24    | -    | ns    |
| t <sub>f</sub>                      | Fall Time                                            | R <sub>D</sub> =0.45Ω                                    | -    | 14    | -    | ns    |
| C <sub>iss</sub>                    | Input Capacitance                                    | V <sub>GS</sub> =0V                                      | -    | 790   | -    | pF    |
| C <sub>oss</sub>                    | Output Capacitance                                   | V <sub>DS</sub> =25V                                     | -    | 475   | -    | pF    |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                         | f=1.0MHz                                                 | -    | 195   | -    | pF    |

### Source-Drain Diode

| Symbol          | Parameter                                         | Test Conditions                                                | Min. | Typ. | Max. | Units |
|-----------------|---------------------------------------------------|----------------------------------------------------------------|------|------|------|-------|
| I <sub>S</sub>  | Continuous Source Current ( Body Diode )          | V <sub>D</sub> =V <sub>G</sub> =0V , V <sub>S</sub> =1.26V     | -    | -    | 66   | A     |
| I <sub>SM</sub> | Pulsed Source Current ( Body Diode ) <sup>1</sup> |                                                                | -    | -    | 210  | A     |
| V <sub>SD</sub> | Forward On Voltage <sup>2</sup>                   | T <sub>j</sub> =25°C, I <sub>S</sub> =66A, V <sub>GS</sub> =0V | -    | -    | 1.26 | V     |

### Drain-Source Avalanche Ratings

| Symbol          | Parameter                                  | Test Conditions                                    | Min. | Typ. | Max. | Units |
|-----------------|--------------------------------------------|----------------------------------------------------|------|------|------|-------|
| E <sub>AS</sub> | Single Pulse Avalanche Energy <sup>2</sup> | V <sub>DD</sub> =25V, I <sub>D</sub> =35A, L=100uH | -    | -    | 61   | mJ    |
| I <sub>AR</sub> | Avalanche Current                          | V <sub>GS</sub> =10V                               | -    | -    | 35   | A     |

#### Notes:

- 1.Pulse width limited by safe operating area.
- 2.Pulse width ≤300us , duty cycle ≤2%.

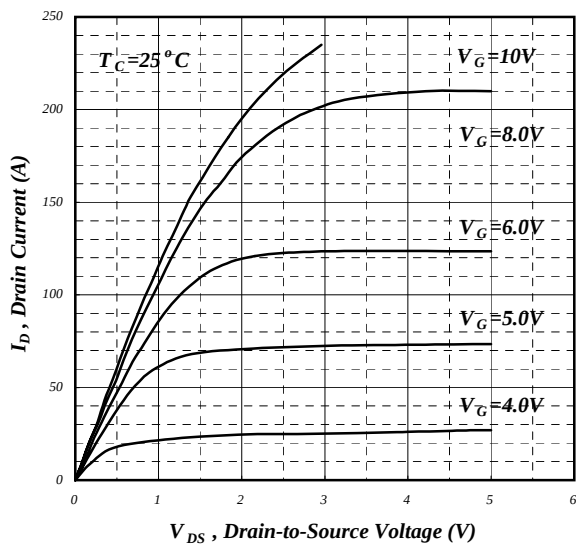


Fig 1. Typical Output Characteristics

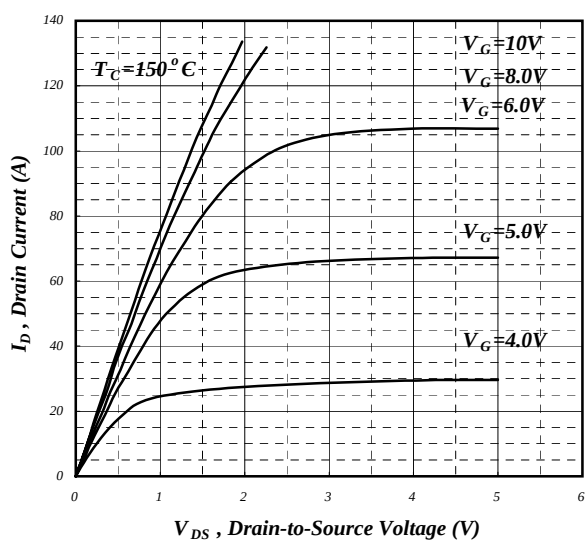


Fig 2. Typical Output Characteristics

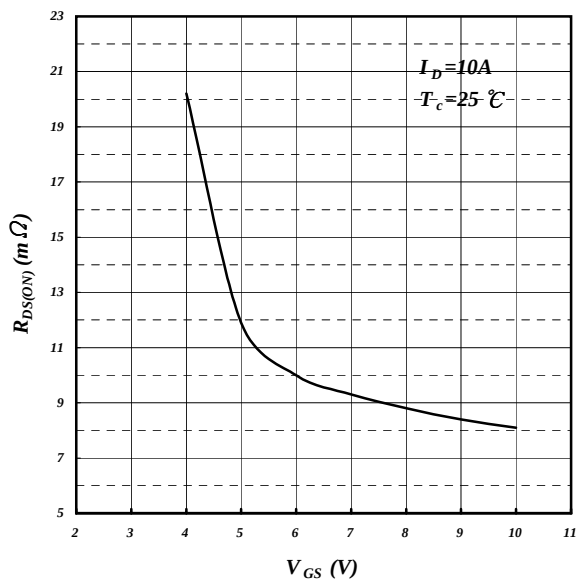


Fig 3. On-Resistance v.s. Gate Voltage

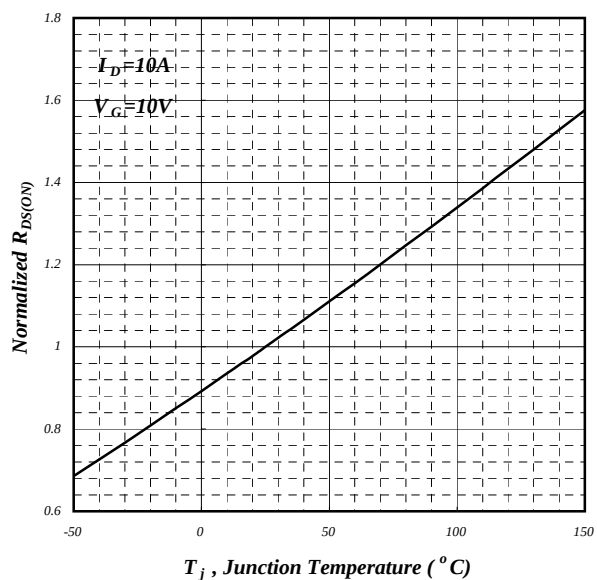


Fig 4. Normalized On-Resistance  
v.s. Junction Temperature

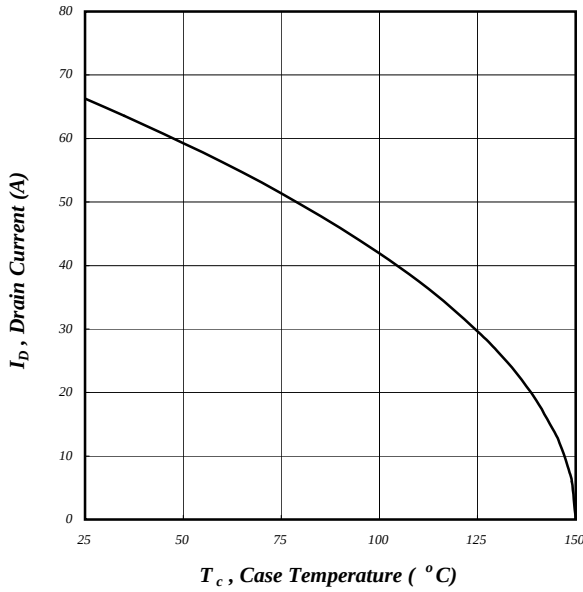


Fig 5. Maximum Drain Current v.s. Case Temperature

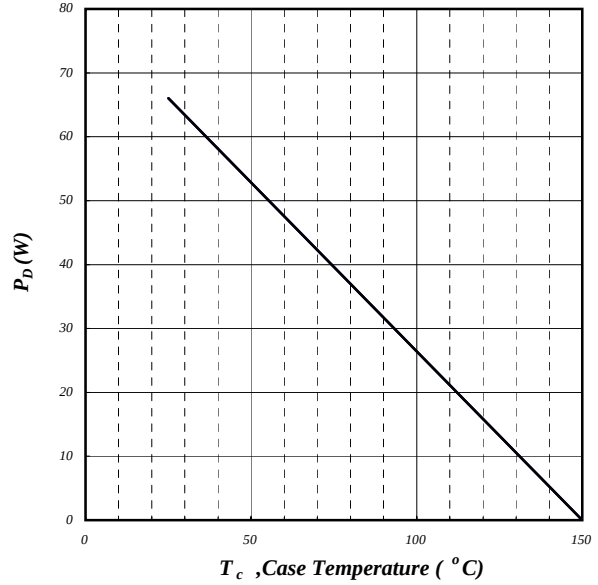


Fig 6. Typical Power Dissipation

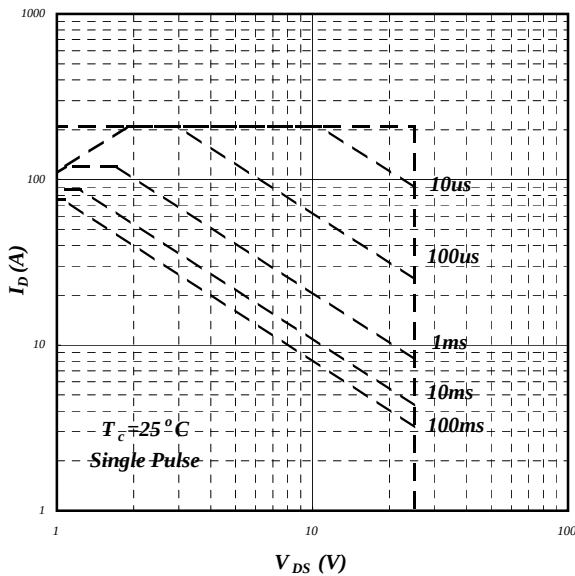


Fig 7. Maximum Safe Operating Area

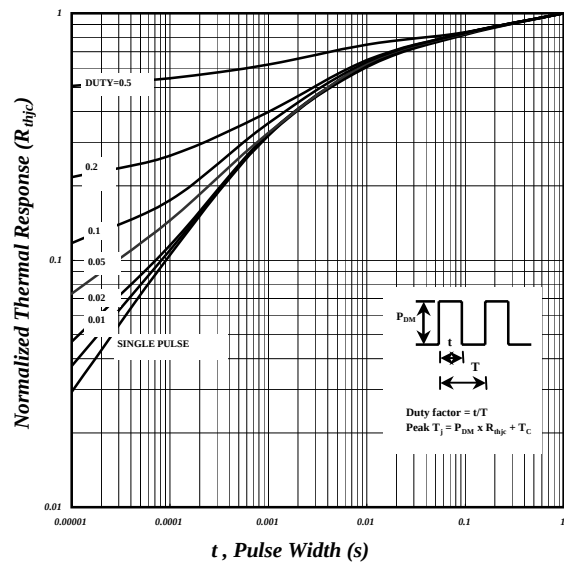
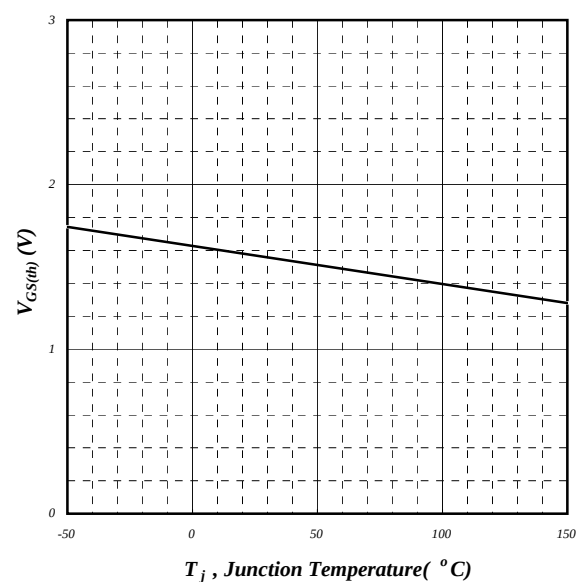
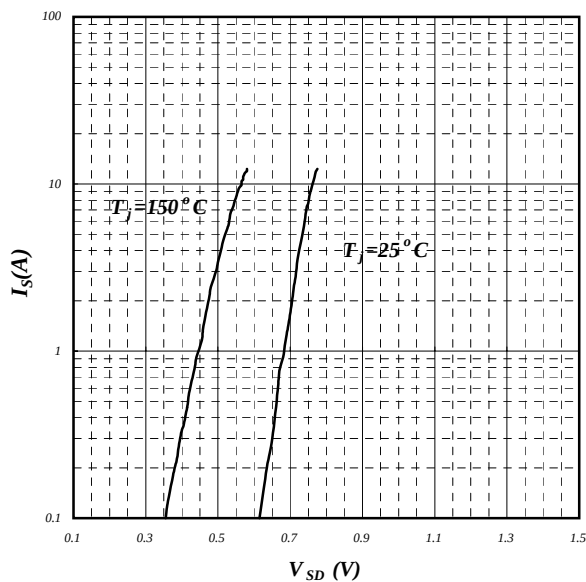
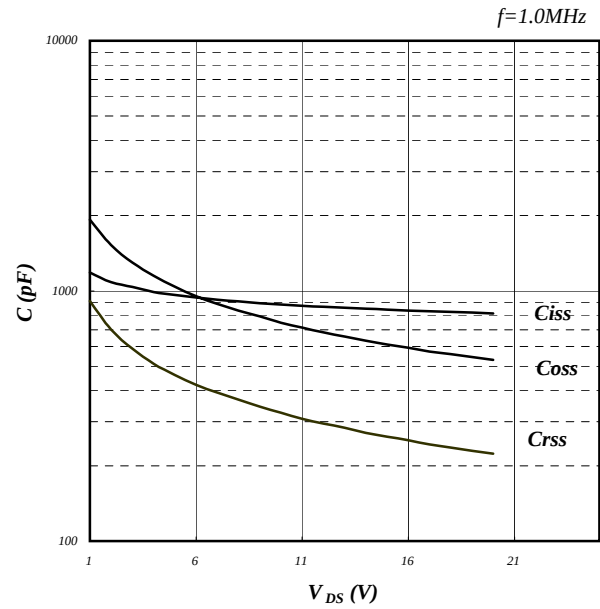
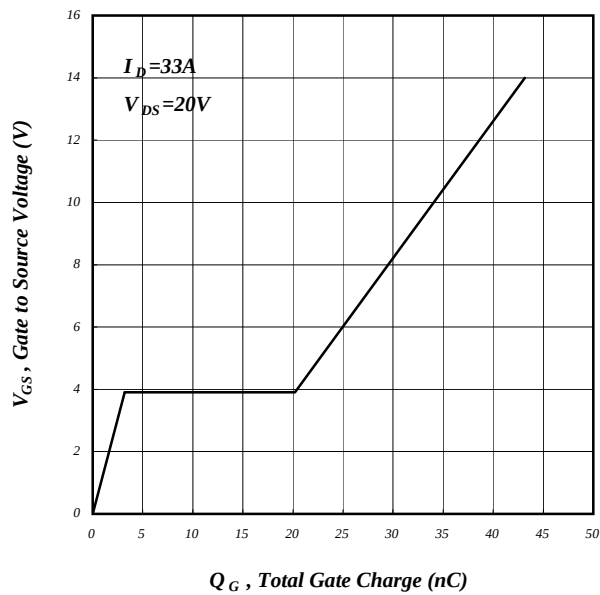


Fig 8. Effective Transient Thermal Impedance



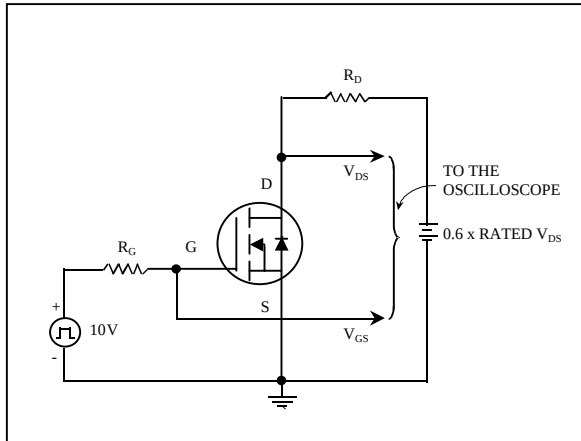


Fig 13. Switching Time Circuit

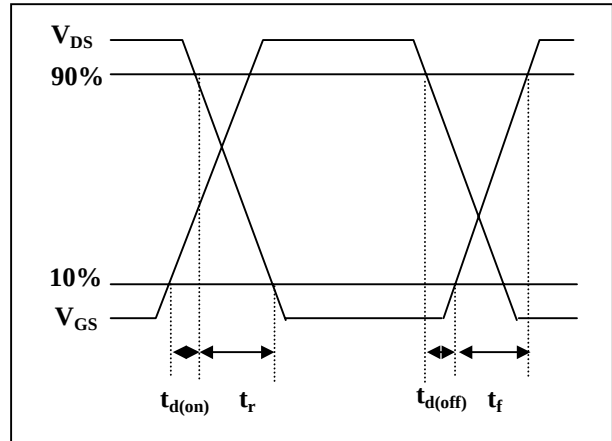


Fig 14. Switching Time Waveform

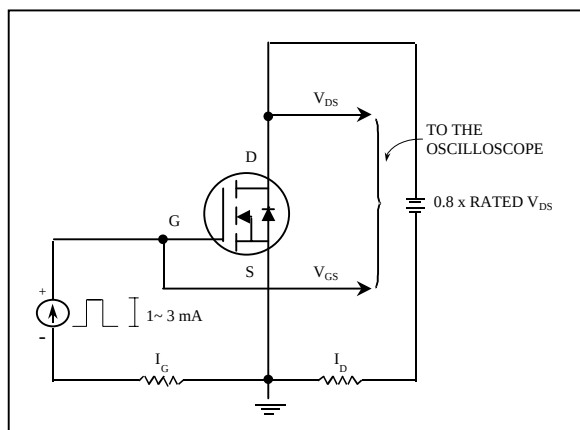


Fig 15. Gate Charge Circuit

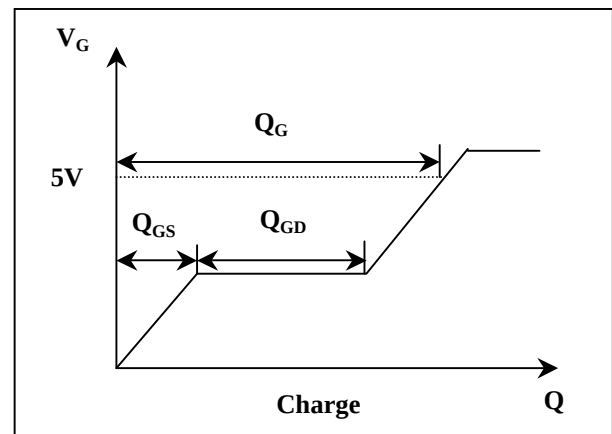


Fig 16. Gate Charge Waveform