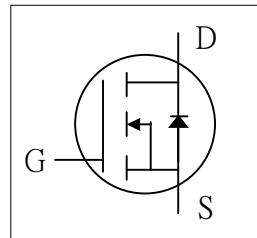
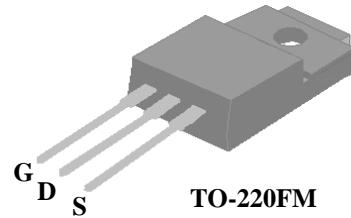




- ▼ 100% Avalanche Test
- ▼ Fast Switching Characteristic
- ▼ Simple Drive Requirement



BV_{DSS}	650V
$R_{DS(ON)}$	2.4Ω
I_D	4A



Description

AP04N70 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications. TO-220FM type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching, ruggedized design and cost-effectiveness.

The TO-220FM package is widely preferred for all commercial-industrial applications. The device is suited for switch mode power supplies, DC-AC converters and high current high speed switching circuits.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	4	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	2.5	A
I_{DM}	Pulsed Drain Current ¹	15	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation	33	W
	Linear Derating Factor	0.26	W/°C
E_{AS}	Single Pulse Avalanche Energy ²	100	mJ
I_{AR}	Avalanche Current	4	A
E_{AR}	Repetitive Avalanche Energy	4	mJ
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	3.8	°C/W
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	65	°C/W



Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=1\text{mA}$	650	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.6	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=2\text{A}$	-	-	2.4	Ω
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\text{\mu A}$	2	-	4	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=2\text{A}$	-	2.5	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=600\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	10	\mu A
	Drain-Source Leakage Current ($T_j=150^\circ\text{C}$)	$V_{\text{DS}}=480\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	100	\mu A
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 30\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge ³	$I_{\text{D}}=4\text{A}$	-	16.7	-	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=480\text{V}$	-	4.1	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	4.9	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time ³	$V_{\text{DD}}=300\text{V}$	-	11	-	ns
t_r	Rise Time	$I_{\text{D}}=4\text{A}$	-	8.3	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=10\Omega$, $V_{\text{GS}}=10\text{V}$	-	23.8	-	ns
t_f	Fall Time	$R_D=75\Omega$	-	8.2	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	950	-	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	65	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	6	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_s	Continuous Source Current (Body Diode)	$V_D=V_G=0\text{V}$, $V_S=1.5\text{V}$	-	-	4	A
I_{SM}	Pulsed Source Current (Body Diode) ¹		-	-	15	A
V_{SD}	Forward On Voltage ³	$T_j=25^\circ\text{C}$, $I_s=4\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	1.5	V

Notes:

1. Pulse width limited by max. junction temperature
2. Starting $T_j=25^\circ\text{C}$, $V_{\text{DD}}=50\text{V}$, $L=25\text{mH}$, $R_G=25\Omega$, $I_{\text{AS}}=4\text{A}$.
3. Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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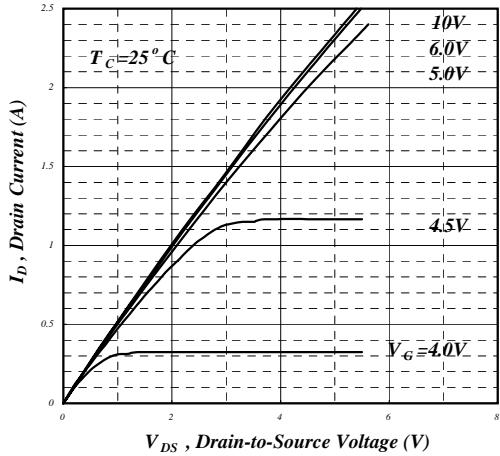


Fig 1. Typical Output Characteristics

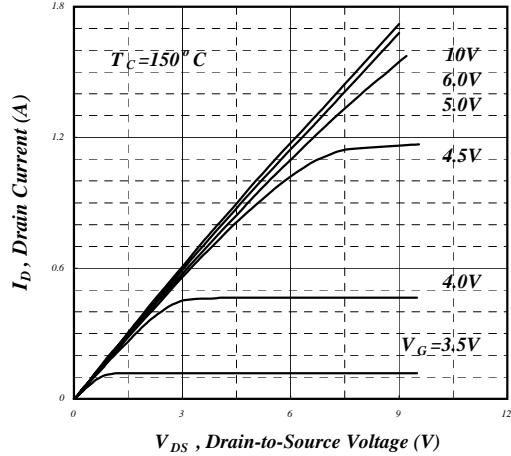


Fig 2. Typical Output Characteristics

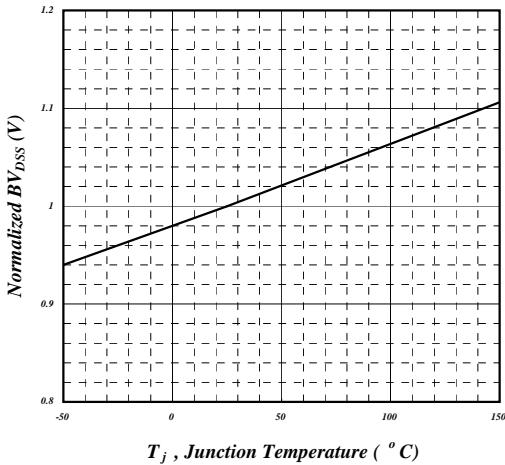
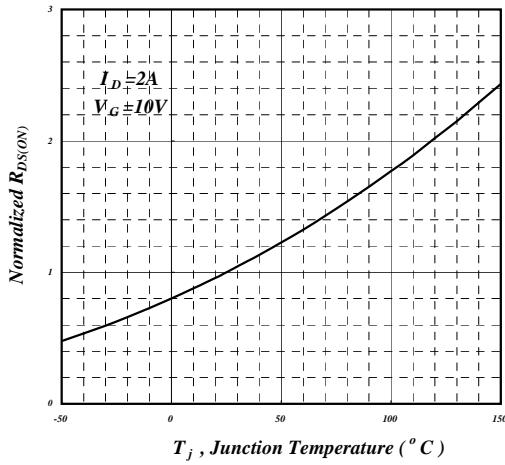
Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

Fig 4. Normalized On-Resistance v.s. Junction Temperature

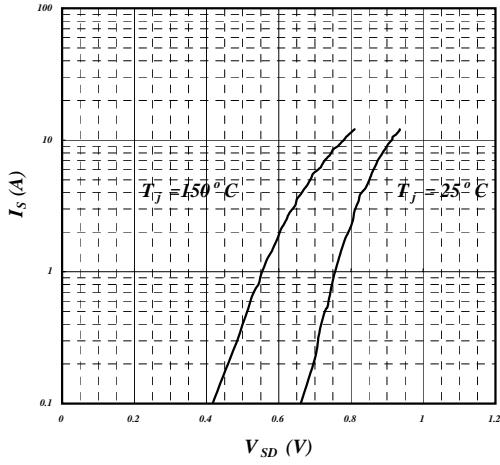


Fig 5. Forward Characteristic of Reverse Diode

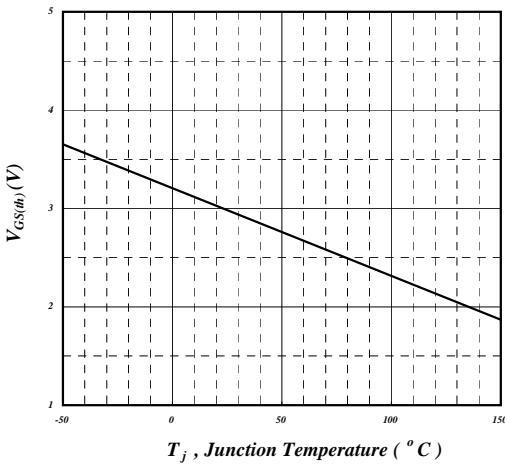
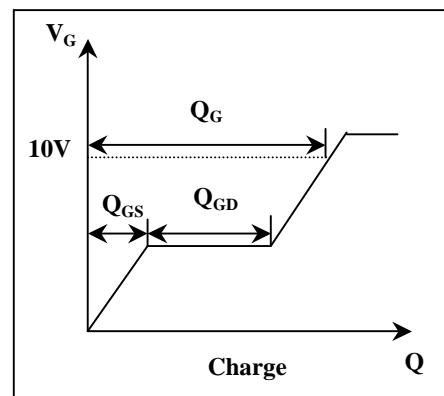
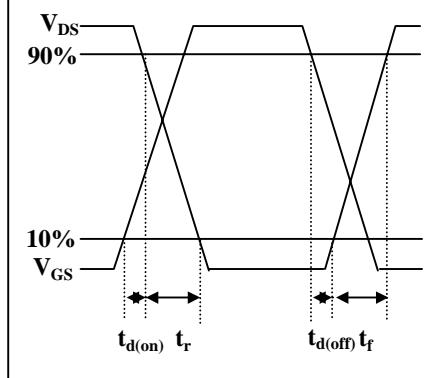
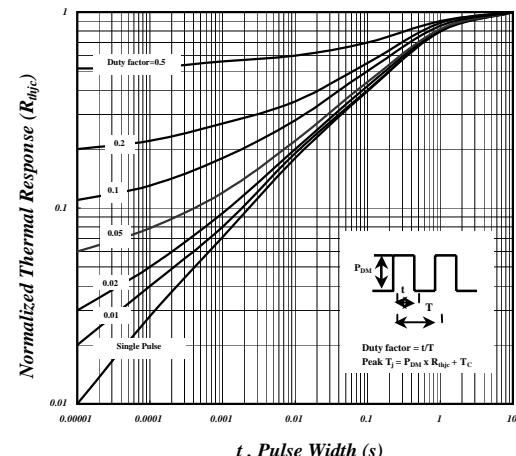
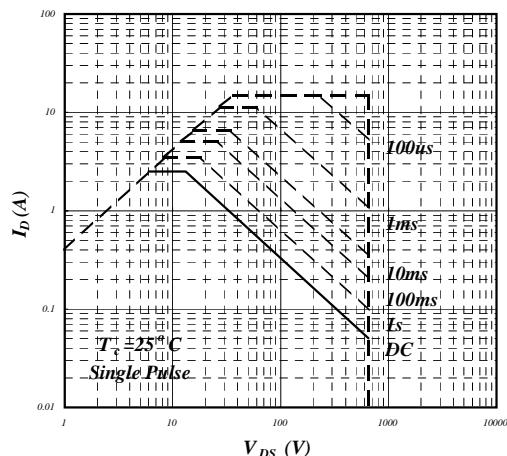
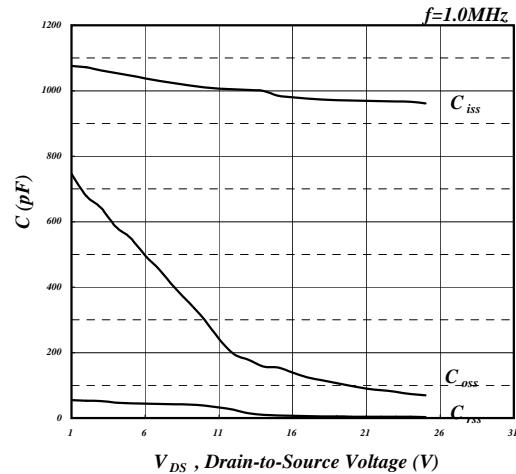
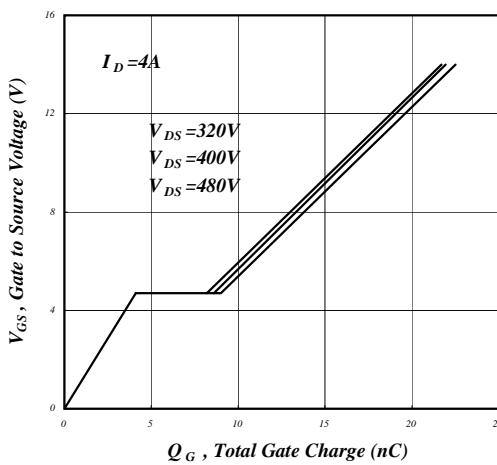


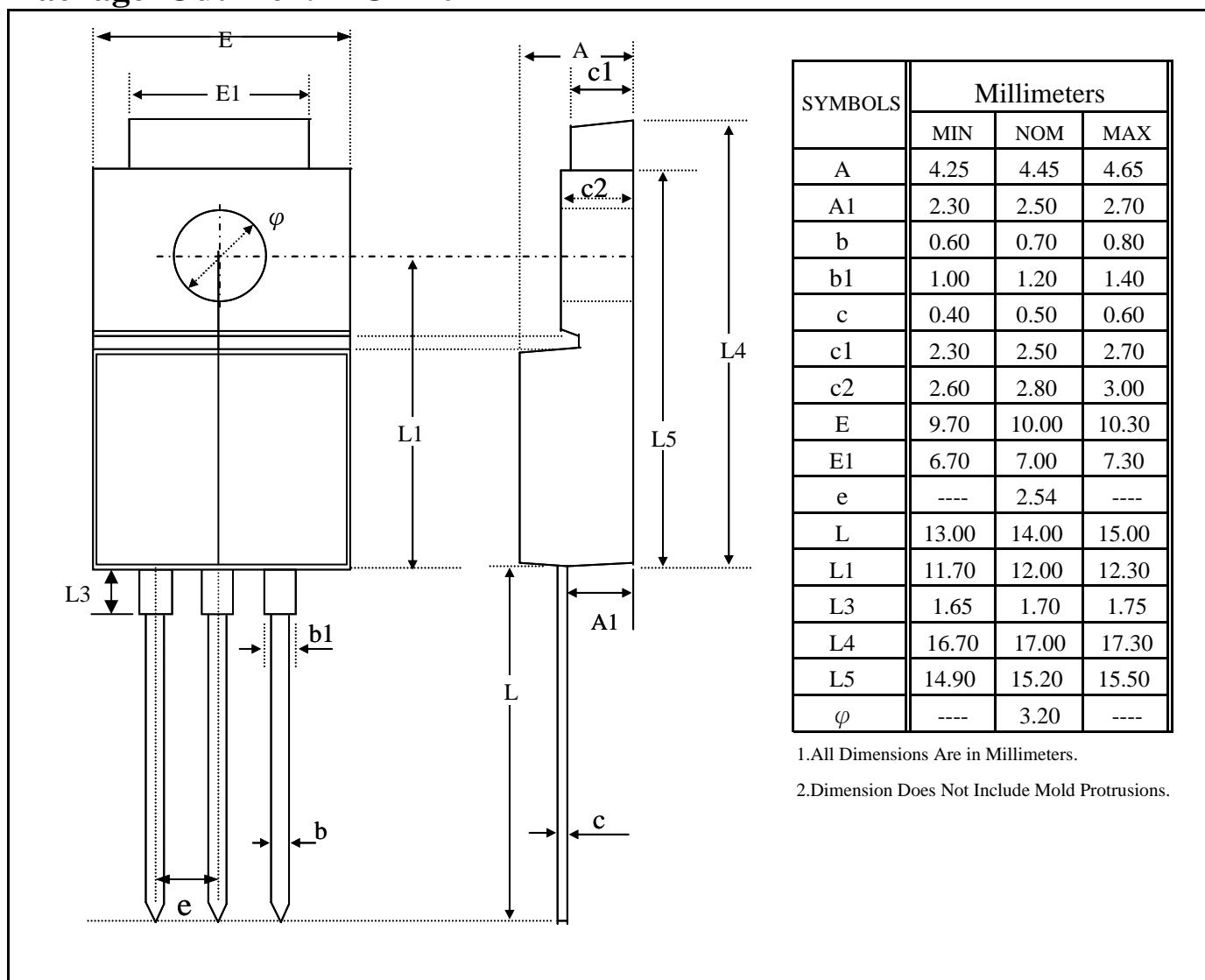
Fig 6. Gate Threshold Voltage v.s. Junction Temperature

AP04N70BF-A





Package Outline : TO-220FM



Part Marking Information & Packing : TO-220FM

