

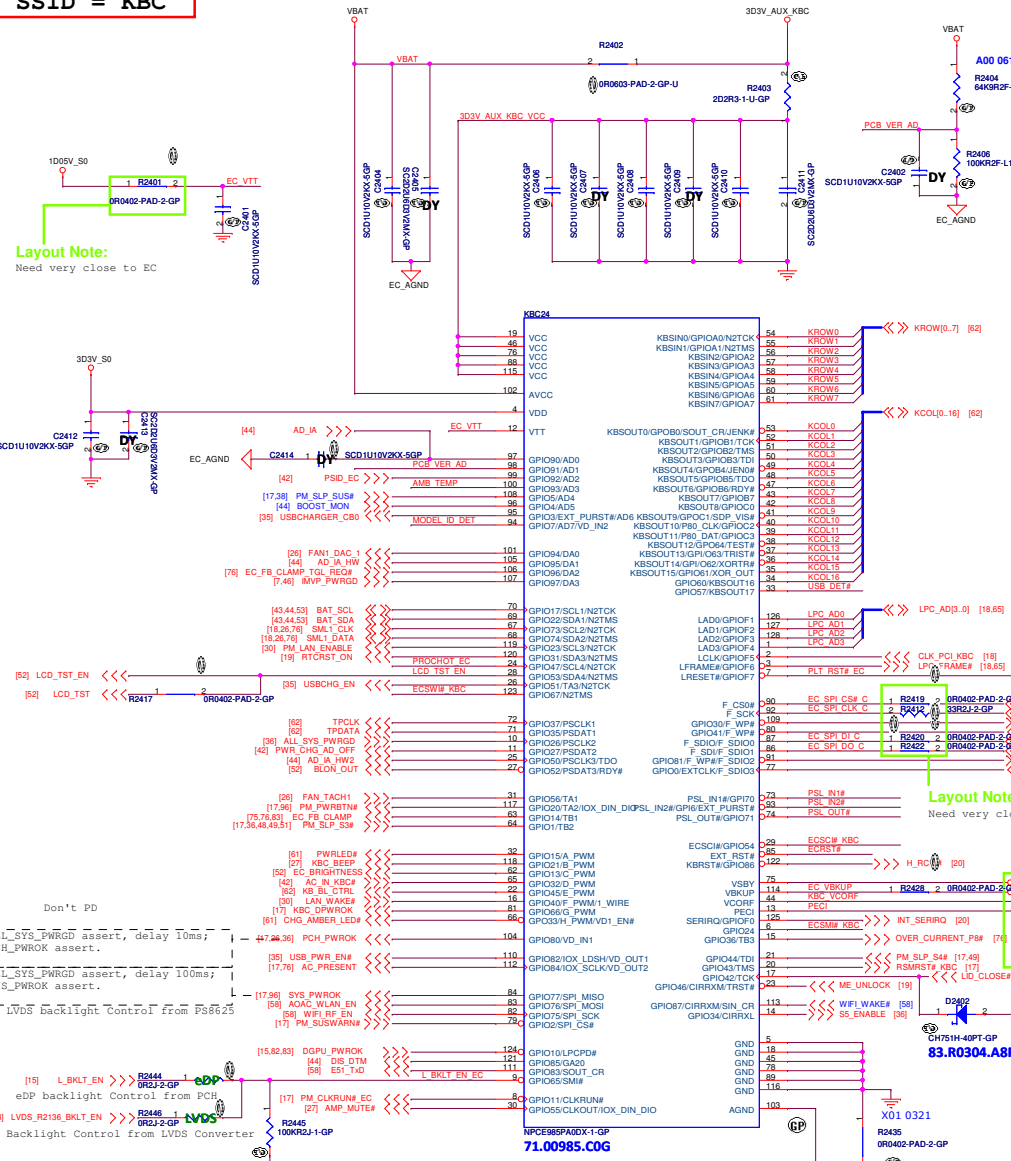
SSID = KBC

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PCB VERSION A/D(PIN#)	PULL-LOW RESISTOR	PULL-HIGH RESISTOR	VOLTAGE
X00	100.0K	10.0K	3.0V
X01	100.0K	20.0K	2.75V
X02	100.0K	33.0K	2.48V
X03	100.0K	47.0K	2.24V
A00	100.0K	64.9K	2.0V
Reserved	100.0K	76.8	1.87V
Reserved	100.0K	100.0K	1.65V
Reserved	100.0K	143.0K	1.38V
Reserved	100.0K	174.0K	1.204V
Reserved	100.0K	215.0K	1.048V

MODEL ID DET	PULL-LOW RESISTOR	PULL-HIGH RESISTOR	VOLTAGE
(DOH50)UMA	100.0K	10.0K(64.10925.GD.L)	3.0V
(DOH50)UMA/dP	100.0K	13.7K(64.13725.GD.L)	2.902V
(DOH70)DIS	100.0K	17.8K(64.17825.GD.L)	2.801V
(DOH70)DIS/dP	100.0K	22.1K(64.22125.GD.L)	2.700V
(DOH50)UMA/LVDS	100.0K	27.0K(64.27025.GD.L)	2.598V
(DOH50)UMA/LVDS/dP	100.0K	32.4K(64.32425.GD.L)	2.497V
(DOH50)DIS	100.0K	37.4K(64.37425.GD.L)	2.402V
(DOH50)DIS/dP	100.0K	43.2K(64.43225.GD.L)	2.301V
(DOH50)UMA/LVDS	100.0K	49.9K(64.49925.GD.L)	2.201V
(DOH50)DIS/LVDS	100.0K	57.6K(64.57625.GD.L)	2.093V
(DOH50)DIS/LVDS/dP	100.0K	64.9K(64.64925.GD.L)	1.992V
(DOH50)DIS/LVDS	100.0K	73.2K(64.73225.GD.L)	1.902V
(DOH50)DIS/LVDS/dP	100.0K	82.5K(64.82525.GD.L)	1.808V
(DOH50)DIS/LVDS	100.0K	93.1K(64.93125.GD.L)	1.709V
(DOH50)DIS/LVDS/dP	100.0K	107.7K(64.10725.GD.L)	1.594V
(DOH50)DIS/LVDS	100.0K	130K(64.13025.GD.L)	1.490V
(DOH50)DIS/LVDS/dP	100.0K	137K(64.13725.GD.L)	1.392V
(DOH50)DIS/LVDS	100.0K	154K(64.15425.GD.L)	1.290V
(DOH50)DIS/LVDS/dP	100.0K	170K(64.17025.GD.L)	1.090V
(DOH50)DIS/LVDS	100.0K	232K(64.23225.GD.L)	0.994V

Layout Note: Need very close to EC



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Layout Note: Connect GND and AGND planes with either DR resistor or connect directly.

