

Silicon NPN Power Transistors

2SD1556

DESCRIPTION

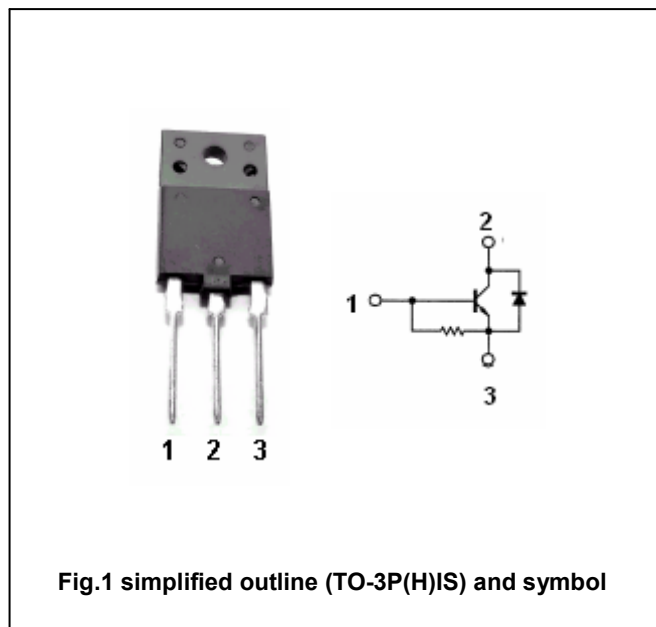
- With TO-3P(H)IS package
- Built-in damper diode
- High voltage ,high speed
- Low collector saturation voltage

APPLICATIONS

- For color TV horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings (Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	600	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		6	A
I_B	Base current		3	A
P_C	Collector power dissipation		50	W
T_j	Junction temperature		150	℃
T_{stg}	Storage temperature		-55~150	℃

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CHARACTERISTICS

Tj=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)EBO}$	Emitter-base breakdown voltage	$I_E=200mA$, $I_C=0$	5			V
V_{CEsat}	Collector-emitter saturation voltage	$I_C=5A$; $I_B=1A$		3.0	5.0	V
V_{BEsat}	Base-emitter saturation voltage	$I_C=5A$; $I_B=1A$			1.5	V
I_{CBO}	Collector cut-off current	$V_{CB}=500V$; $I_E=0$			10	μA
h_{FE}	DC current gain	$I_C=1A$; $V_{CE}=5V$	8			
f_T	Transition frequency	$I_C=0.1A$; $V_{CE}=10V$		3		MHz
C_{OB}	Collector output capacitance	$I_E=0$; $V_{CB}=10V$; $f=1MHz$		165		pF
V_F	Diode forward voltage	$I_F=6A$		1.6	2.0	V
t_f	Fall time	$I_{CP}=5A$; $I_{B1(end)}=1A$		0.5	1.0	μs

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PACKAGE OUTLINE

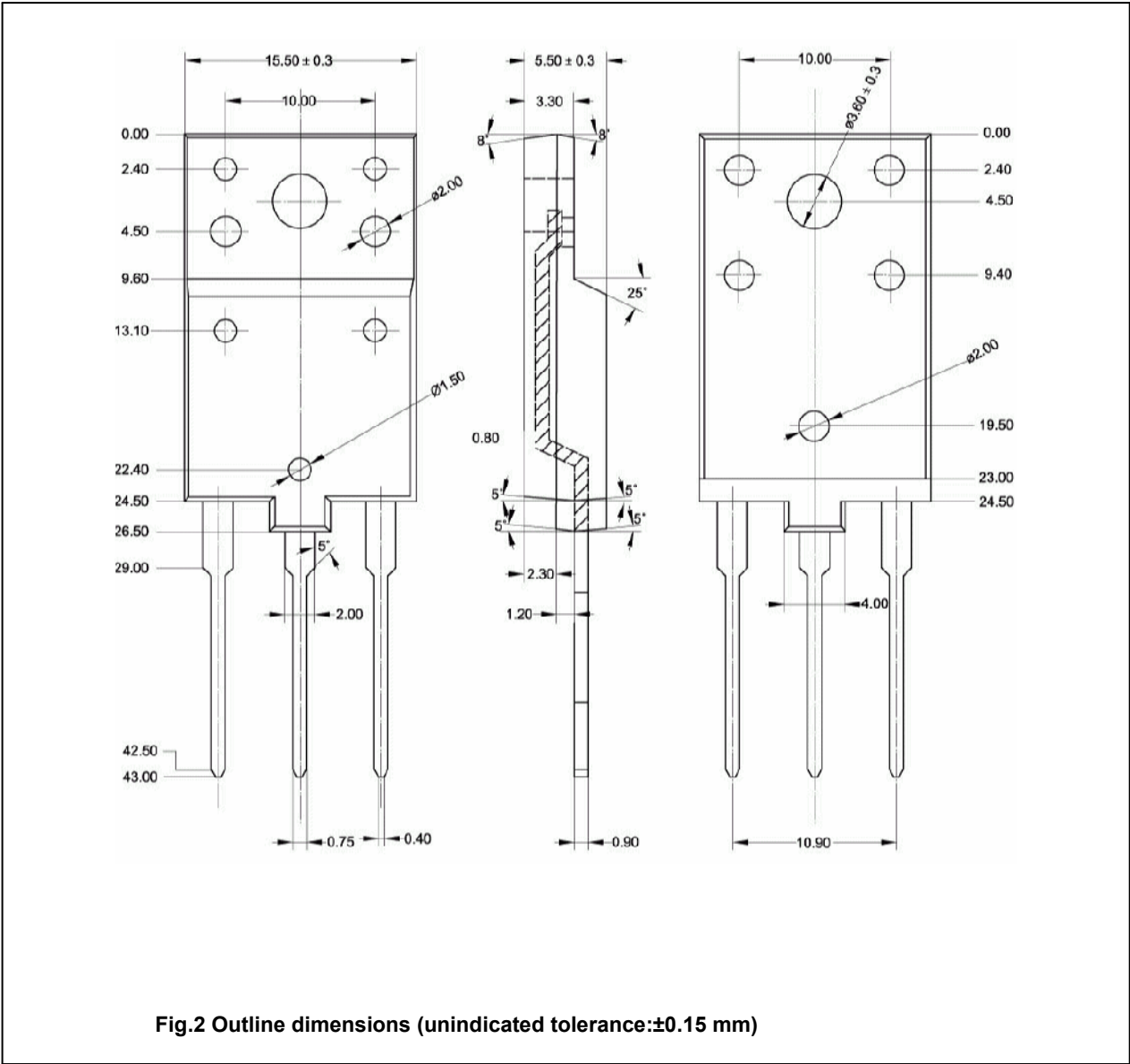


Fig.2 Outline dimensions (unindicated tolerance:±0.15 mm)