

SANYO

No.1069C

2SC3150

NPN Triple Diffused Planar Silicon Transistor
FOR SWITCHING REGULATOR

Features

- High breakdown voltage ($V_{CBO} \geq 900V$)
- High speed switching.
- Wide ASO.

Absolute Maximum Ratings at $T_a = 25^\circ C$

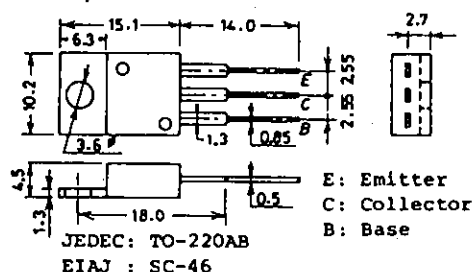
			unit
Collector-to-Base Voltage	V_{CBO}	900	V
Collector-to-Emitter Voltage	V_{CEO}	800	V
Emitter-to-Base Voltage	V_{EBO}	7	V
Collector Current	I_C	3	A
Peak Collector Current	i_{cp}	$PW \leq 300\mu s,$ Duty Cycle $\leq 10\%$	10 A
Base Current	I_B	1.5	A
Collector Dissipation	P_C	$T_C = 25^\circ C$	50 W
Junction Temperature	T_j	150	$^\circ C$
Storage Temperature	T_{stg}	-55 to +150	$^\circ C$

Electrical Characteristics at $T_a = 25^\circ C$

			min	typ	max	unit
Collector Cutoff Current	I_{CBO}	$V_{CB} = 800V, I_E = 0$			10	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB} = 5V, I_C = 0$			10	μA
DC Current Gain	$h_{FE}(1)$	$V_{CE} = 5V, I_C = 0.2A$	10*		40*	
	$h_{FE}(2)$	$V_{CE} = 5V, I_C = 1A$	8			
Gain-Bandwidth Product	f_T	$V_{CE} = 10V, I_C = 0.2A$		15		MHz
Output Capacitance	c_{ob}	$V_{CB} = 10V, f = 1MHz$		60		pF
C-E Saturation Voltage	$V_{CE}(sat)$	$I_C = 1.5A, I_B = 0.3A$			2.0	V
B-E Saturation Voltage	$V_{BE}(sat)$	$I_C = 1.5A, I_B = 0.3A$			1.5	V
C-B Breakdown Voltage	$V_{(BR)CBO}$	$I_C = 1mA, I_E = 0$	900			V
C-E Breakdown Voltage	$V_{(BR)CEO}$	$I_C = 5mA, R_{BE} = \infty$	800			V
E-B Breakdown Voltage	$V_{(BR)EBO}$	$I_E = 1mA, I_C = 0$	7			V
C-E Sustain Voltage	$V_{CEO}(sus)$	$I_C = 3A, L = 500\mu H, I_B = 1A$	800			V
	$V_{CEX}(sus)(1)$	$I_C = 1A, I_{B1} = 0.2A, I_{B2} = -0.2A, L = 2mH, \text{clamped}$	800			V
	$V_{CEX}(sus)(2)$	$I_C = 0.5A, I_{B1} = 0.1A, I_{B2} = -0.1A, L = 5mH, \text{clamped}$	900			V
Turn-ON Time	t_{on}	$\left[\begin{array}{l} I_C = 2A, I_{B1} = 0.4A, I_{B2} = -0.8A, \\ R_L = 200\Omega, V_{CC} = 400V \end{array} \right]$			1.0	μs
Storage Time	t_{stg}				3.0	μs
Fall Time	t_f				0.7	μs

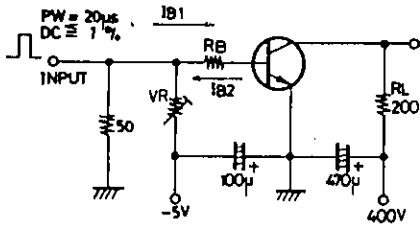
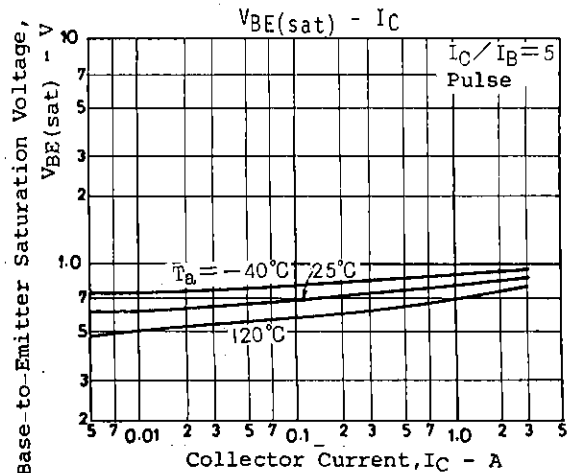
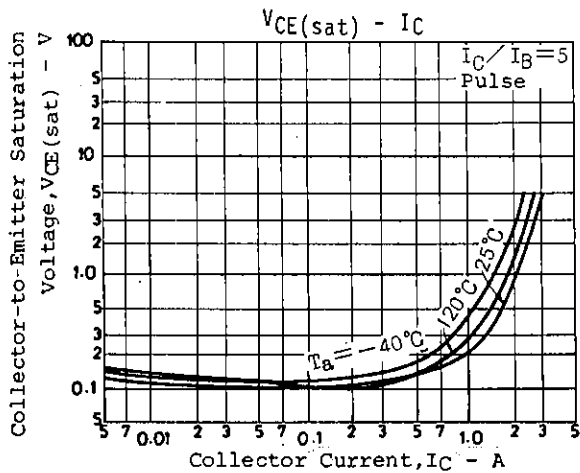
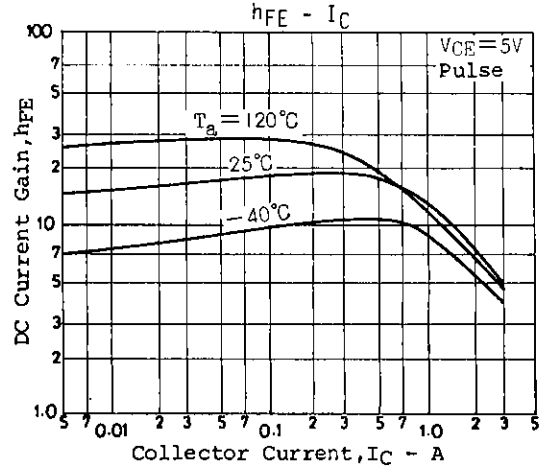
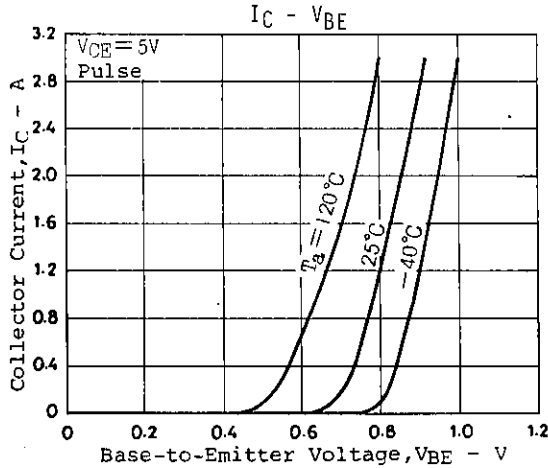
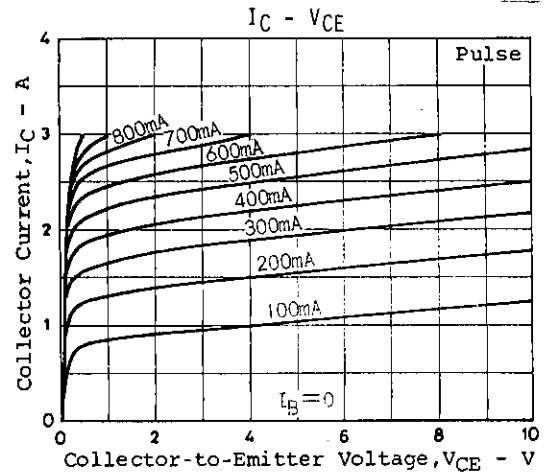
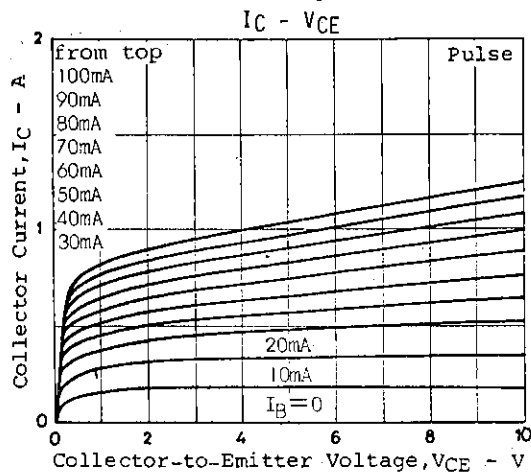
*: The $h_{FE}(1)$ of the 2SC3150 is classified as follows. When specifying the $h_{FE}(1)$ rank, specify two ranks or more in principle.

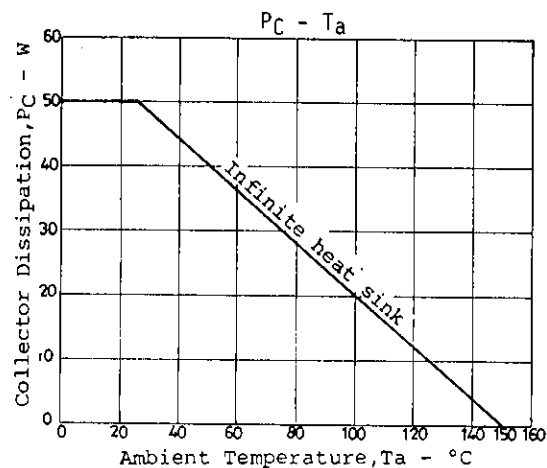
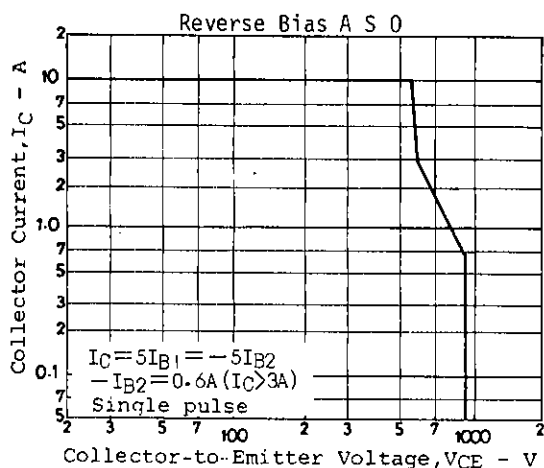
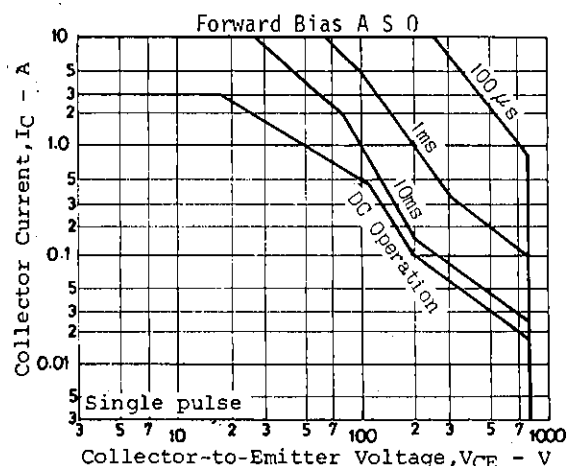
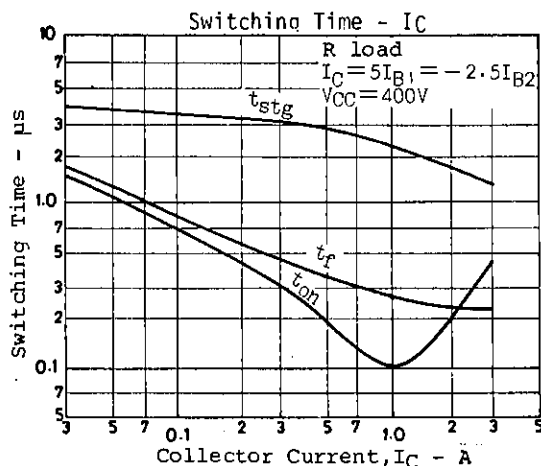
10	K	20	15	L	30	20	M	40
----	---	----	----	---	----	----	---	----

Package Dimensions 2010A
(unit:mm)

SANYO Electric Co., Ltd. Semiconductor Business Headquarters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

Switching Time Test Circuit

Unit (Resistance : Ω , Capacitance : F)



■ No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.

■ Anyone purchasing any products described or contained herein for an above-mentioned use shall:

- ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use;
- ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.

■ Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.