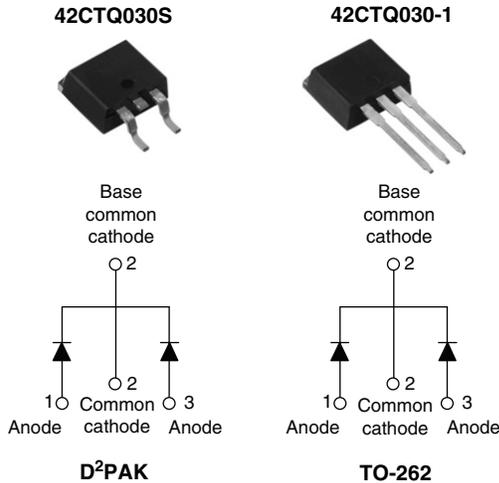


## Schottky Rectifier, 2 x 20 A



### FEATURES

- 150 °C  $T_J$  operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified for Q101 level

### DESCRIPTION

This center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

### PRODUCT SUMMARY

$I_{F(AV)}$	2 x 20 A
$V_R$	30 V

### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	40	A
$V_{RRM}$		30	V
$I_{FSM}$	$t_p = 5 \mu s$ sine	1100	A
$V_F$	20 Apk, $T_J = 125 \text{ }^\circ\text{C}$ (per leg)	0.38	V
$T_J$	Range	- 55 to 150	$^\circ\text{C}$

### VOLTAGE RATINGS

PARAMETER	SYMBOL	42CTQ030S 42CTQ030-1	UNITS
Maximum DC reverse voltage	$V_R$	30	V
Maximum working peak reverse voltage	$V_{RWM}$		

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 121 \text{ }^\circ\text{C}$ , rectangular waveform	20	A
			40	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	$I_{FSM}$	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	110	A
		10 ms sine or 6 ms rect. pulse		
Non-repetitive avalanche energy per leg	$E_{AS}$	$T_J = 25 \text{ }^\circ\text{C}$ , $I_{AS} = 3 \text{ A}$ , $L = 2.90 \text{ mH}$	13	mJ
Repetitive avalanche current per leg	$I_{AR}$	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical	3	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	20 A	$T_J = 25\text{ }^\circ\text{C}$	0.48	V
		40 A		0.57	
		20 A	$T_J = 125\text{ }^\circ\text{C}$	0.38	
		40 A		0.51	
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	3	mA
		$T_J = 125\text{ }^\circ\text{C}$		183	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.22	V
Forward slope resistance	$r_t$			6.76	m $\Omega$
Maximum junction capacitance per leg	$C_T$	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^\circ\text{C}$		2840	pF
Typical series inductance per leg	$L_S$	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$		10 000	V/ $\mu\text{s}$

**Note**(1) Pulse width < 300  $\mu\text{s}$ , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$			- 55 to 150	$^\circ\text{C}$
Maximum thermal resistance, junction to case per leg	$R_{thJC}$	DC operation		2.0	$^\circ\text{C/W}$
Maximum thermal resistance, junction to case per package				1.0	
Typical thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, smooth and greased (Only for TO-262)		0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device		Case style D <sup>2</sup> PAK		42CTQ030S	
		Case style TO-262		42CTQ030-1	

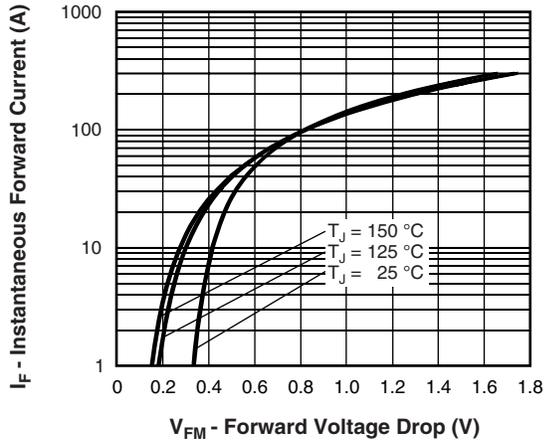


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

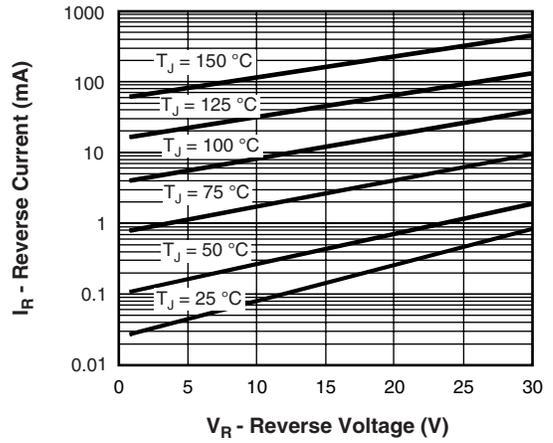


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

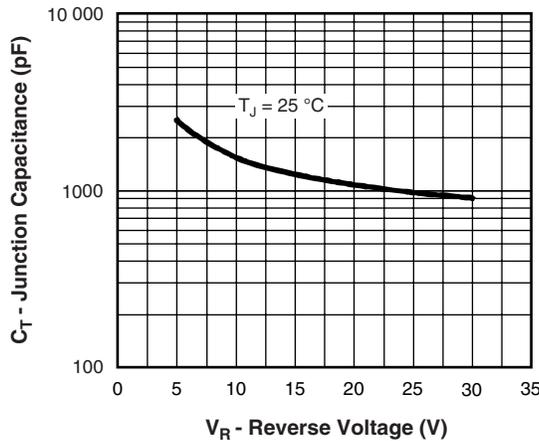


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

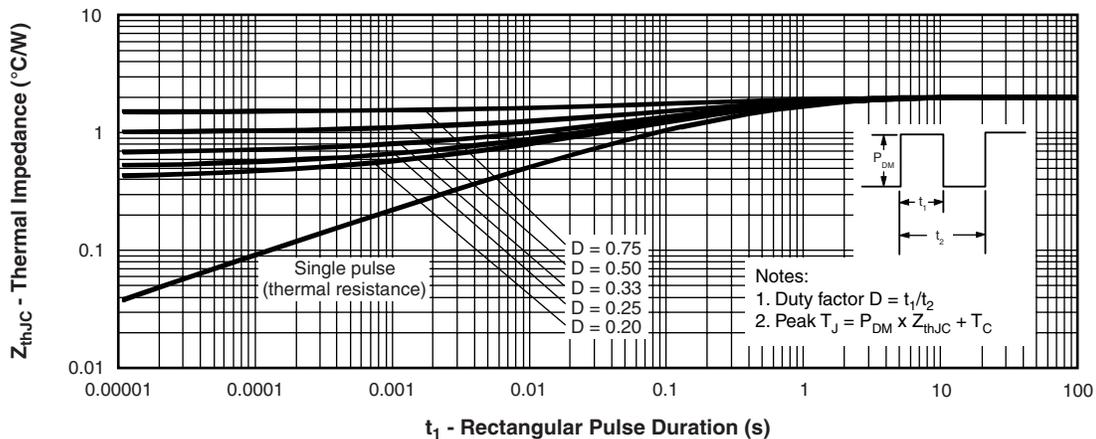


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics (Per Leg)

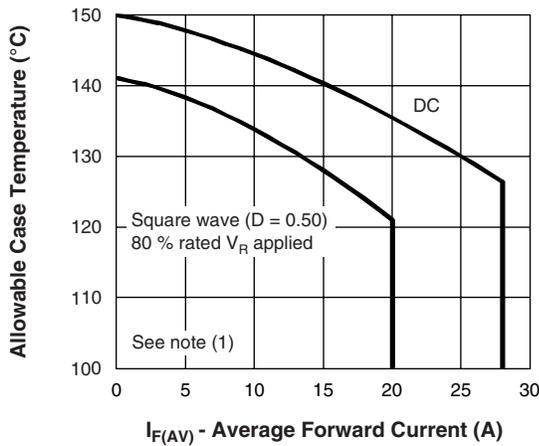


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

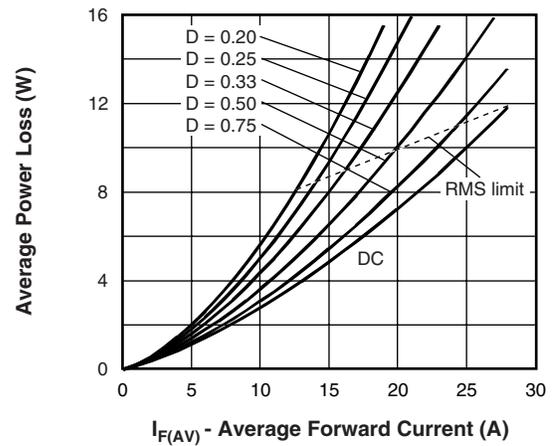


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

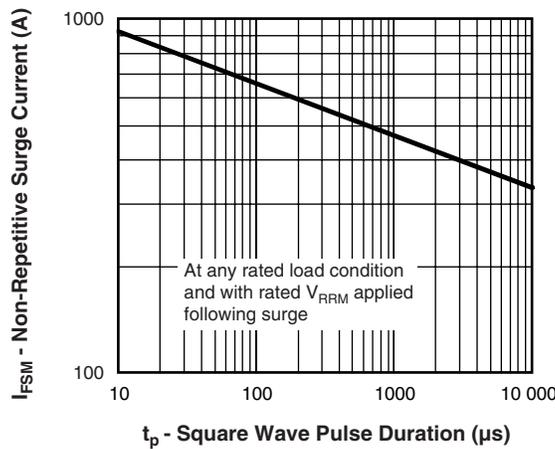


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

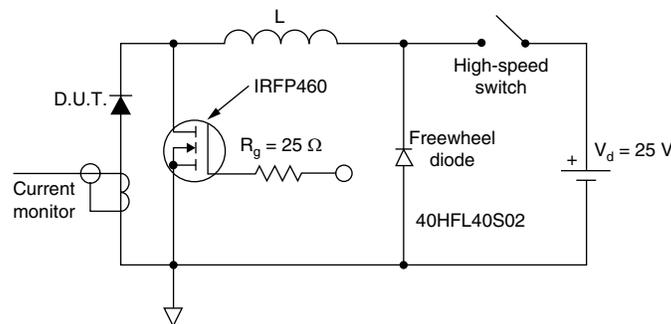


Fig. 8 - Unclamped Inductive Test Circuit

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $Pd$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);
- $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 10 V$



## ORDERING INFORMATION TABLE

Device code	<b>42</b>	<b>C</b>	<b>T</b>	<b>Q</b>	<b>030</b>	<b>S</b>	<b>TRL</b>	<b>-</b>
	①	②	③	④	⑤	⑥	⑦	⑧

- 1** - Current rating (40 A)
- 2** - Circuit configuration:  
C = Common cathode
- 3** - T = TO-220
- 4** - Schottky "Q" series
- 5** - Voltage rating (030 = 30 V)
- 6** -
  - S = D<sup>2</sup>PAK
  - -1 = TO-262
- 7** -
  - None = Tube (50 pieces)
  - TRL = Tape and reel (left oriented - for D<sup>2</sup>PAK only)
  - TRR = Tape and reel (right oriented - for D<sup>2</sup>PAK only)
- 8** -
  - None = Standard production
  - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95014">http://www.vishay.com/doc?95014</a>
Part marking information	<a href="http://www.vishay.com/doc?95008">http://www.vishay.com/doc?95008</a>
Packaging information	<a href="http://www.vishay.com/doc?95032">http://www.vishay.com/doc?95032</a>



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